

SAR 14108

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Robertson Tower et al.
Serial No.: To Be Assigned
Filed: Herewith
FOR: CCD IMAGER CONSTRUCTED
WITH CMOS FABRICATION
TECHNIQUES AND BACK
ILLUMINATED IMAGER WITH
IMPROVED LIGHT CAPTURE

: Art Unit:
: Examiner:

5 / IDS
E. Villio
3-28-02



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

S I R :

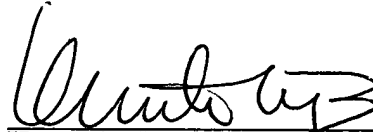
Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (R&P) submitted herewith. A copy of each of the listed references is also enclosed.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

No fee or certification is required. 37 C.F.R. § 1.97(b).

Respectfully submitted,



Kenneth N. Nigon, Reg. No. 31,549
Attorney for Applicants

Encls.: PTO Form 1449,
Copy of (22) References


Dated: August 30, 2001

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The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail under Express Mail Label No. EL 910 364 526 US in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:

August 30, 2001



Kathleen Libby